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Serial No. : 09/942,102
Filed : August 29, 2001
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Attorney's Docket No.: 10559-595001
Intel Docket No.: P12879

IN THE SPECIFICATION:

Please amend the paragraph beginning at page 1, line 19 as follows:

A textual representation describes the logic elements of the computer chip using one-dimensional text lines. Textual representations are used in hardware description languages (HDLs) which allow designers to simulate logic designs prior to forming the logic on silicon. Examples of such languages include Verilog and Very <u>High Speed Integrated Circuit (VHSIC) Hardware Description</u> ~~High Level Design~~ Language (VHDL). Using these languages, a designer can write code to simulate a logic design and execute the code in order to determine if the logic design performs properly.

Please amend the paragraph beginning at page 6, line 21 as follows:

Process 10 embeds the combinatorial data block into the two-dimensional schematic presentation to complete the unified data base (22). Thus, the unified data base is a complete representation of the IC and can be represented in RTDs.